

10050 U.S. PTO  
10/073134  
02/11/02

U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

|   |                           |              |               |                                     |                      |
|---|---------------------------|--------------|---------------|-------------------------------------|----------------------|
| APPL NUM<br>10073134  | FILING DATE<br>02/11/2002 | CLASS<br>257 | SUBCLASS<br>- | GAU<br>2841                         | EXAMINER<br>TAN TRAN |
| **APPLICANTS: Shimizume Kazutoshi;  |                           |              |               |                                     |                      |
| **CONTINUING DATA VERIFIED: <i>yes IT</i>   |                           |              |               |                                     |                      |
| **FOREIGN APPLICATIONS VERIFIED: <i>yes IT</i>  |                           |              |               |                                     |                      |
| PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>  |                           |              |               |                                     |                      |
| Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no<br>35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no<br>Verified and Acknowledged Examiners's initials <i>TT</i> |                           |              |               | ATTORNEY DOCKET NO<br>09792909-5338 |                      |
| TITLE : Semiconductor device carrying a plurality of circuits   |                           |              |               |                                     |                      |

U.S. DEPT. OF COMM./PAT. & TM.-P10-436L (Rev. 12-94)

|  |           |                    |  |                      |                      |
|--|-----------|--------------------|--|----------------------|----------------------|
| NOTICE OF ALLOWANCE MAILED   |           | Assistant Examiner |  | CLAIMS ALLOWED       |                      |
|  |           |                    |  | Total Claims         | Print Claim for O.G. |
| ISSUE FEE  |           | Primary Examiner   |  | DRAWING              |                      |
| Amount Due   | Date Paid |                    |  | Sheets Drwg.         | Figs. Drwg.          |
| <input type="checkbox"/> TERMINAL<br>DISCLAIMER  |           | PREPARED FOR ISSUE |  | Application Examiner |                      |
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